Which parts of the 1-bit adder/subtractor belong in inside the 1-bit ALU?
What should we do with the full adder’s Cin input?
Where will “Sub” signal come from?
To what should the logic unit’s control signals (R0, R1) be connected?
How do we select between the adder and the logic unit?
How do we control the selection?

Negative:

Zero:

Overflow:

Please bring this completed handout to discussion section/lab.